

ACKNOWLEDGEMENT RECEIPT

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**Title of
Invention**

METHOD OF MANUFACTURING GATE, THIN FILM TRANSISTOR
AND PIXEL

Submission Type : Utility Patent Filing

Application Number:

10/711835



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First Named Applicant: Ta-Jung Su

Attorney Docket Number: 13129-US-PA

Timestamp: 2004-10-08 01:17:13 EDT

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File Listing:

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us-assignment	us-assignment.dtd	11123	2004-10-08
us-assignment	13129assignment01.tif	48172	2004-10-08
us-assignment	13129assignment02.tif	14196	2004-10-08
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us-fee-sheet	13129-usfees.xml	1630	2004-10-08
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application-body	mathml2.dtd	54588	2004-10-08
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application-body	soextblx.dtd	12870	2004-10-08
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application-body	wipo.ent	4956	2004-10-08
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abstract-pdf	13129usf-abst.pdf	15355	2004-10-08
claims-pdf	13129usf-clms.pdf	22015	2004-10-08
description-pdf	13129usf-desc.pdf	43043	2004-10-08
drawings-pdf	13129usf-draw.pdf	50050	2004-10-08
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us-declaration	13129declaration02.tif	35158	2004-10-08
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